

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,511,586 B2

APPLICATION NO. : 10/550,827

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INVENTOR(S) : Mattisson

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 8, Line 46, Equation 5, delete " $\sigma_{V_{T-2}} nV / \sqrt{W \cdot L_{eff}}$ " and
insert - - $\sigma_{V_T} \sim 2nV / \sqrt{W \cdot L_{eff}}$ - -, therefor.

In Column 11, Lines 40-41, delete "transistros." and insert - - transistors. - -, therefor.

In Column 14, Line 67, in Claim 1, delete "supply:" and insert - - supply; - -, therefor.

In Column 15, Line 3, in Claim 2, delete "to," and insert - - to - -, therefor.

In Column 15, Line 48, in Claim 8, delete "Gapacitor" and insert - - capacitor - -, therefor.

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